

## ABSTRACT:

To provide an electric or electronic circuit arrangement (100), it is proposed that at least one signal-generating unit (40), particularly at least an oscillator unit is connected to the contact terminals (22, 27) of the integrated circuit, the output frequency ( $f_{\text{meas.}}$ ) of which unit is substantially determined by the specific capacitance (C), the signal-generating

5 unit (40) precedes at least a first counting unit (50) which is clocked at the output frequency ( $f_{\text{meas.}}$ ) of the signal-generating unit (40), in which counting unit an actual value count can be determined after a predetermined temporal counting period, at least a second counting unit (55) clocked at a reference frequency ( $f_{\text{ref.}}$ ) is provided, in which counting unit a nominal value count can be determined after the predetermined temporal counting period, the first 10 counting unit (50) and the second counting unit (55) precede at least one comparator unit (60) for comparing the actual value count with the nominal value count, while the functions of the integrated circuit can be blocked and/or locked and/or interrupted temporarily or permanently in the case of an error indication which occurs when the actual value count is compared with the nominal value count.

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Fig. 2